

Title: NOISE REDUCED ELECTRIC POWER  
CONVERSION DEVICE

**Inventor(s):** Jijin Wang, et al.

Appln. No. TBA

Docket #: 41575-274

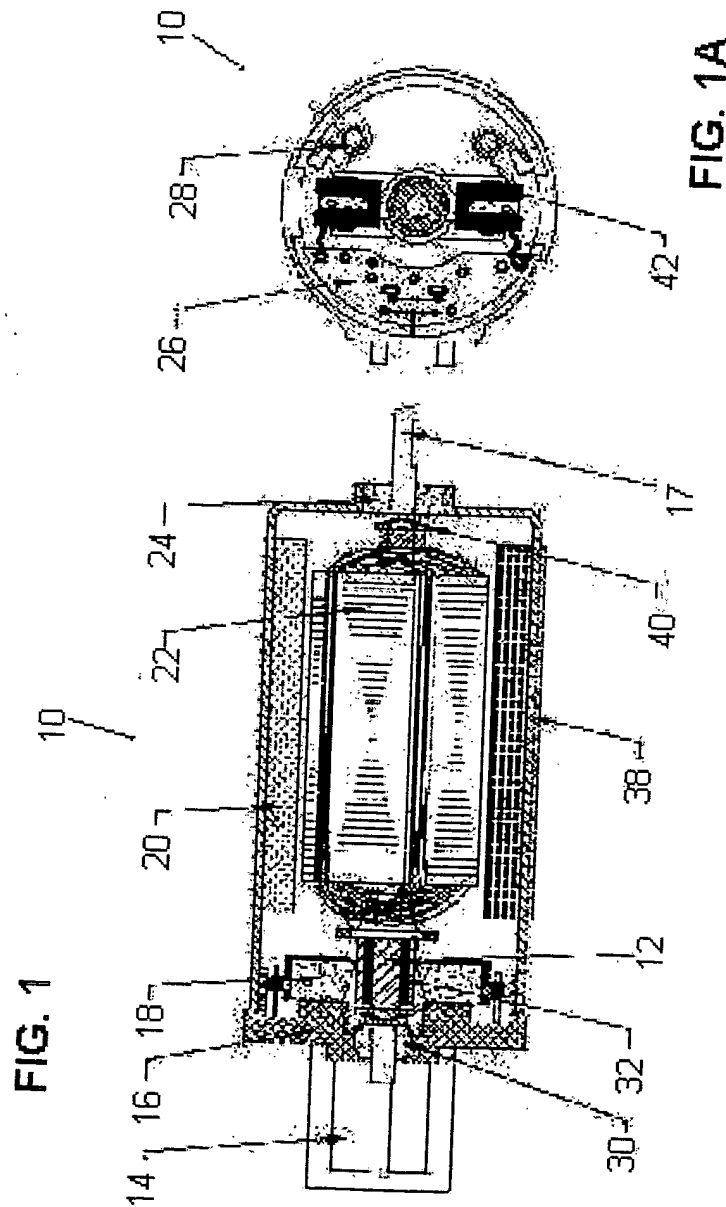


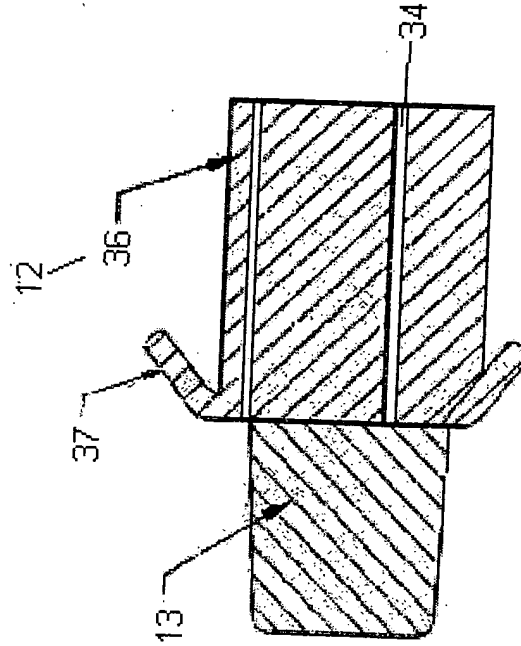
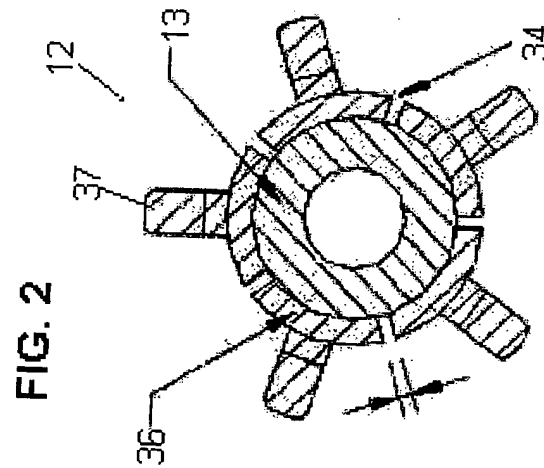
FIG. 1A

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**FIG. 2A**

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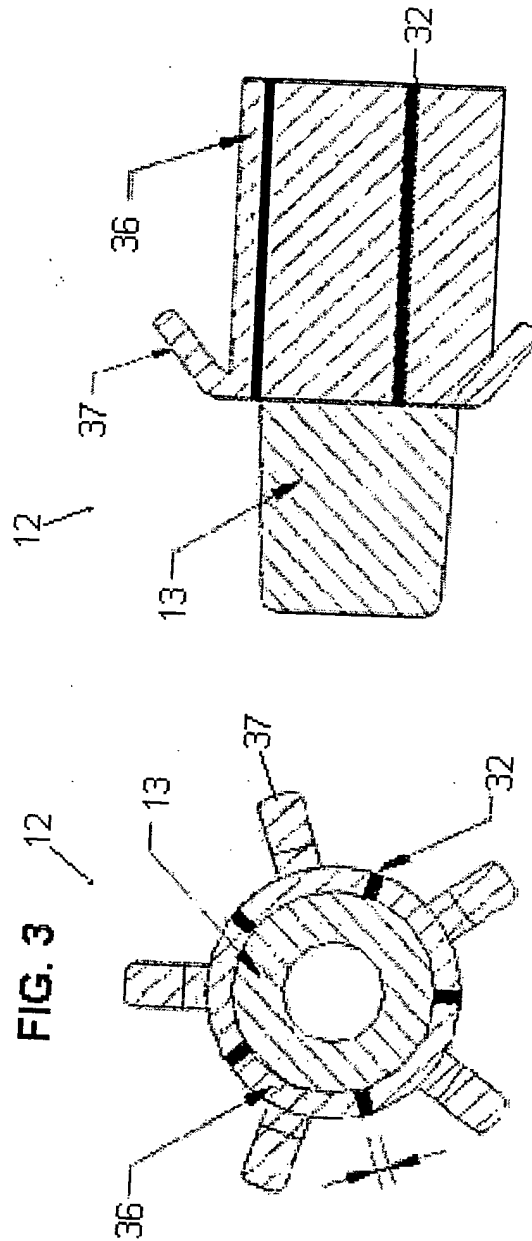


FIG. 3A

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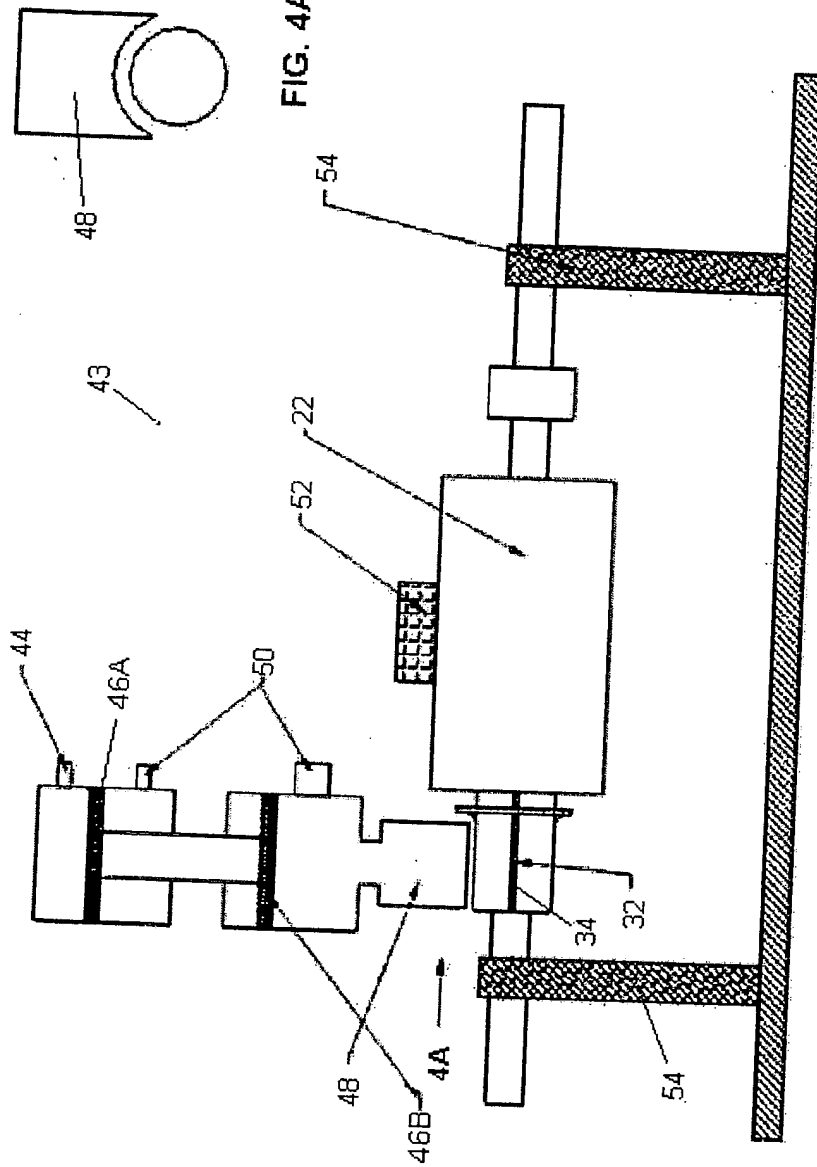
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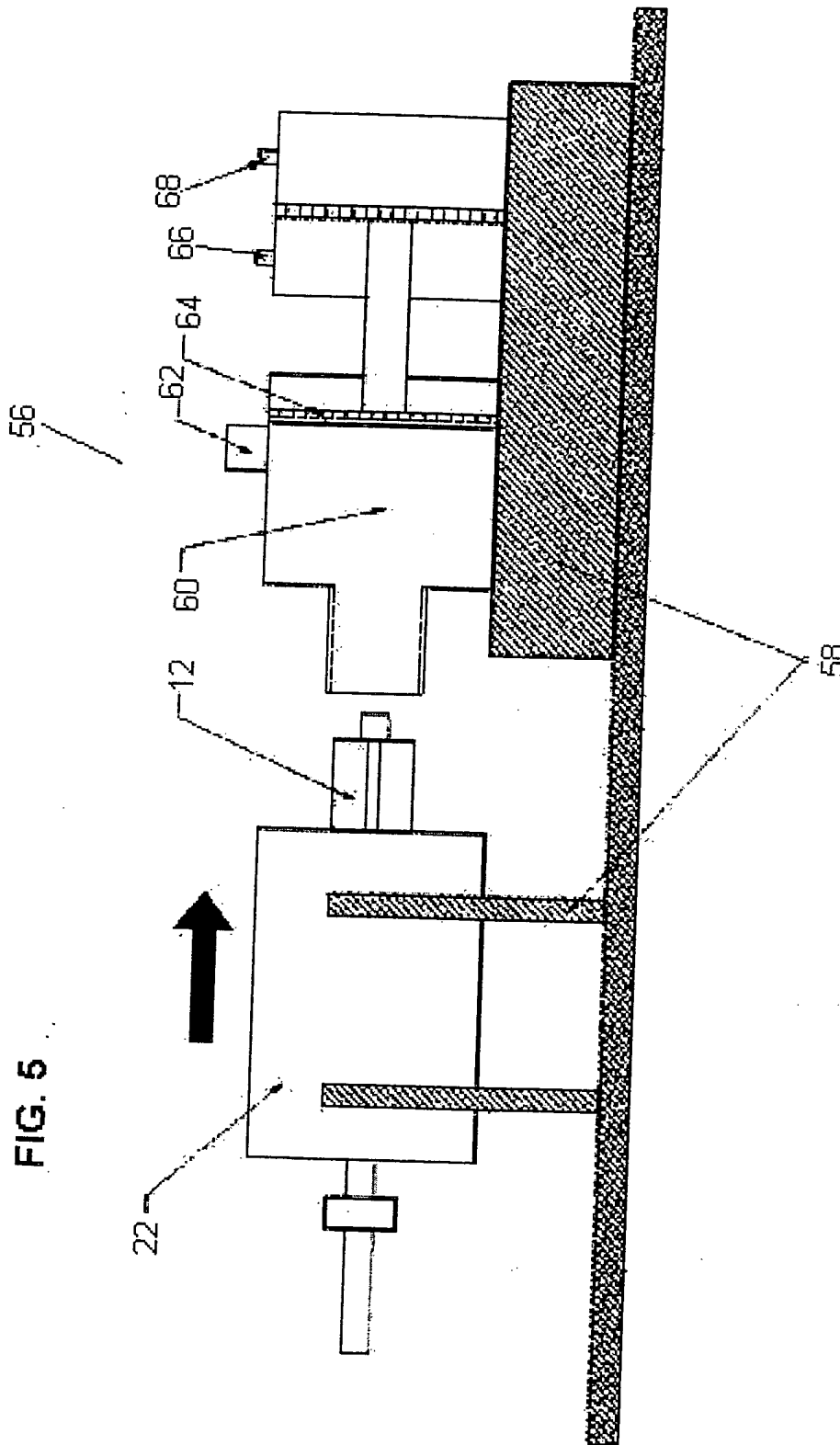
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FIG. 4



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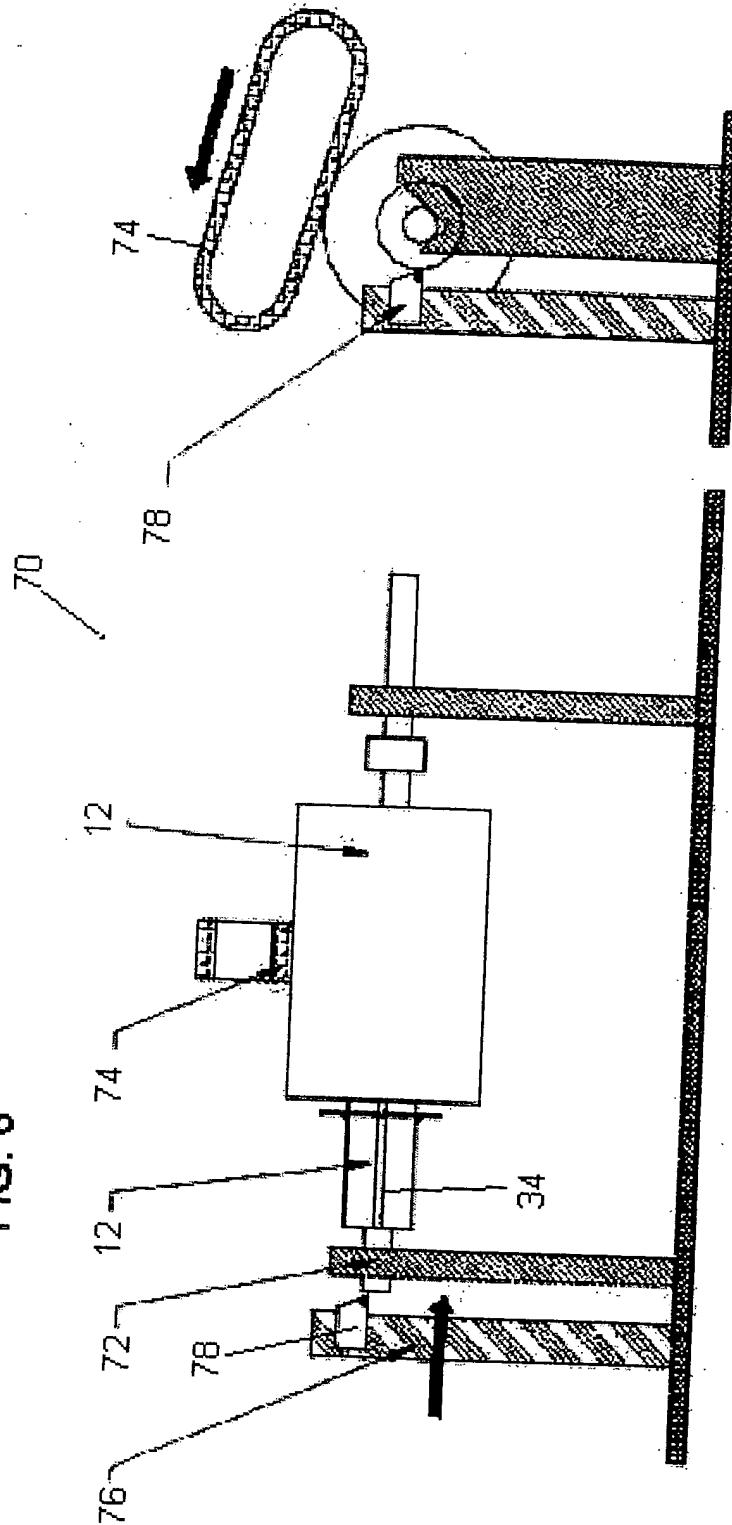
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FIG. 6



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Figure 7. Audible Noise Test Results

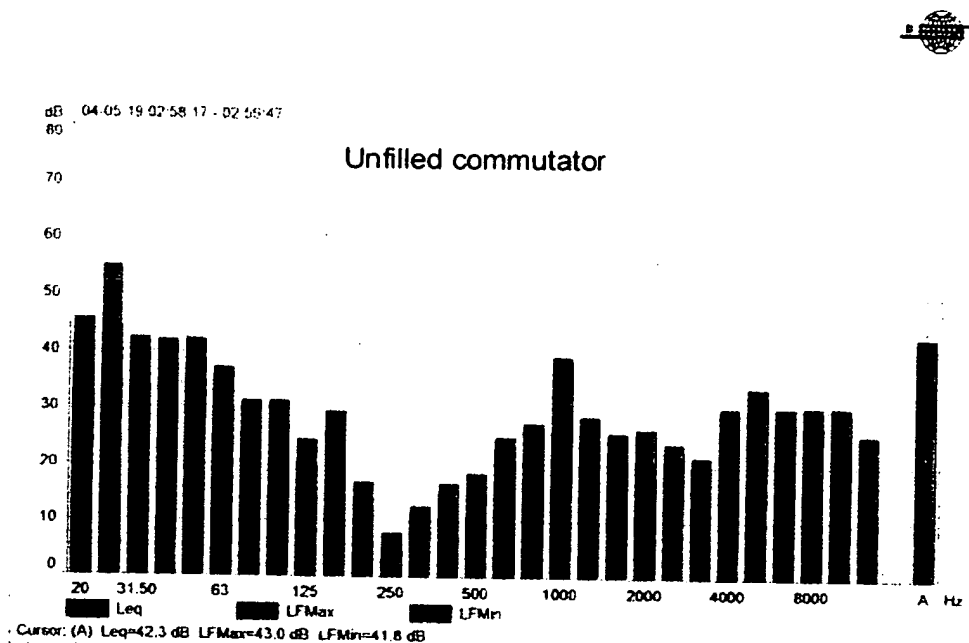
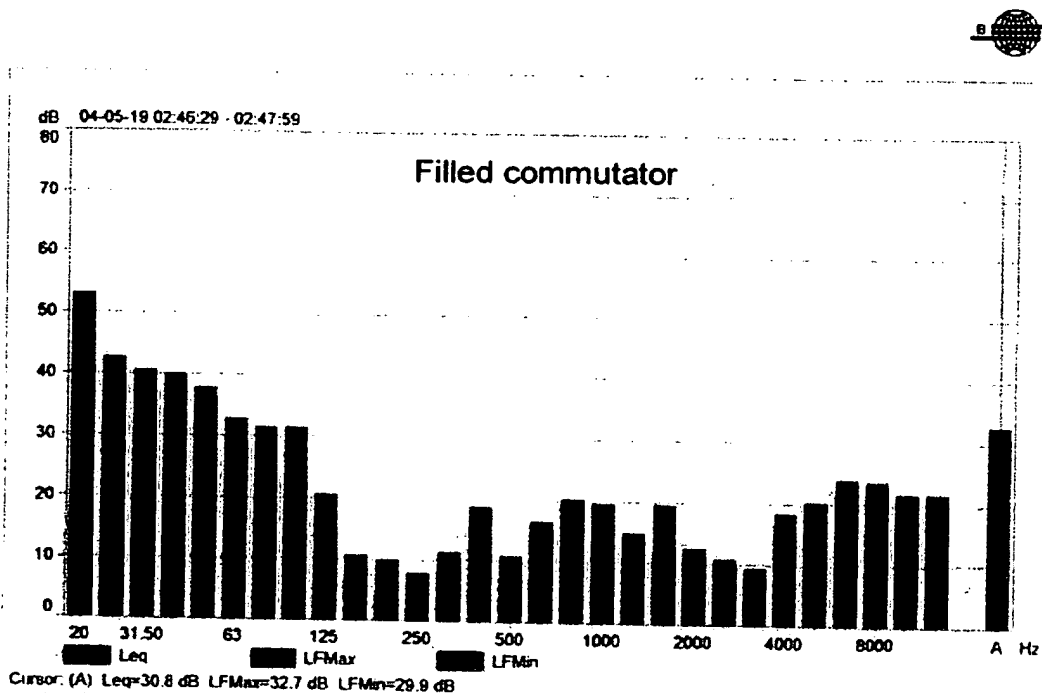


Figure 7A

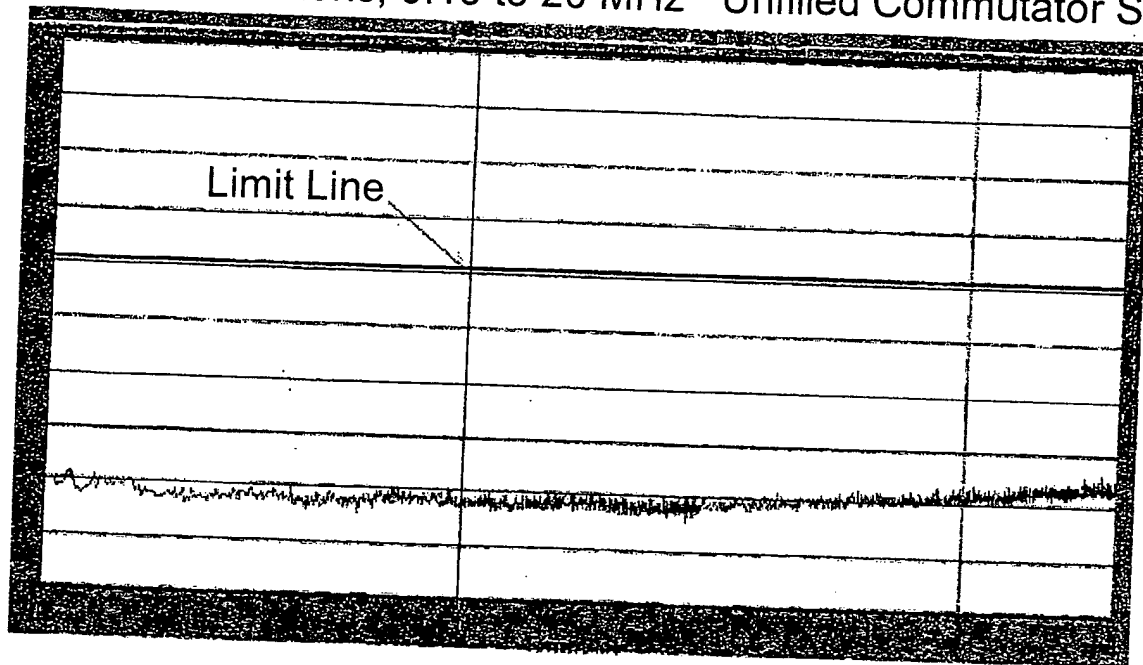


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FIG. 8A

Radiated emissions, 0.15 to 20 MHz Unfilled Commutator Slots



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FIG. 8B

Radiated emissions, 0.15 to 20 MHz Filled Commutator Slots

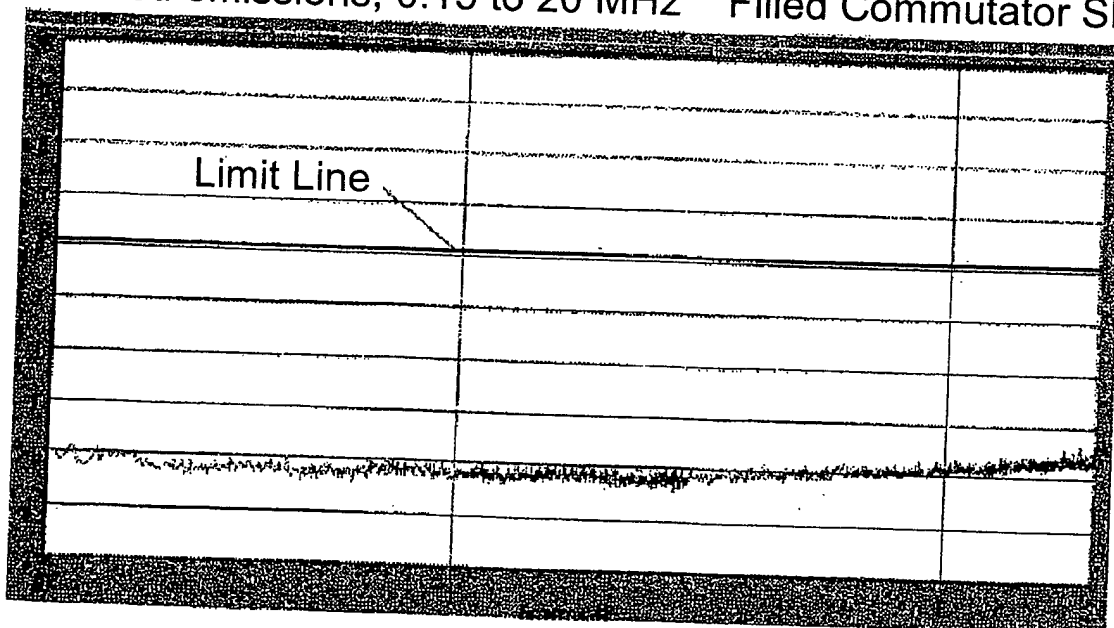




FIG. 8C

Radiated emissions, 20 to 200 MHz Unfilled Commutator Slots

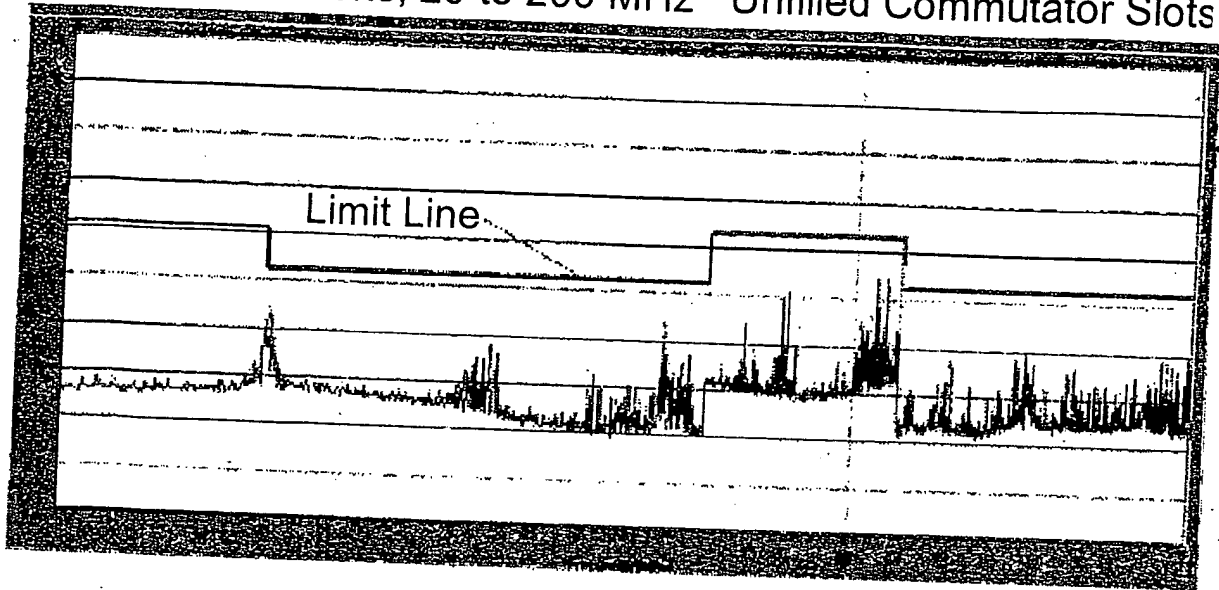
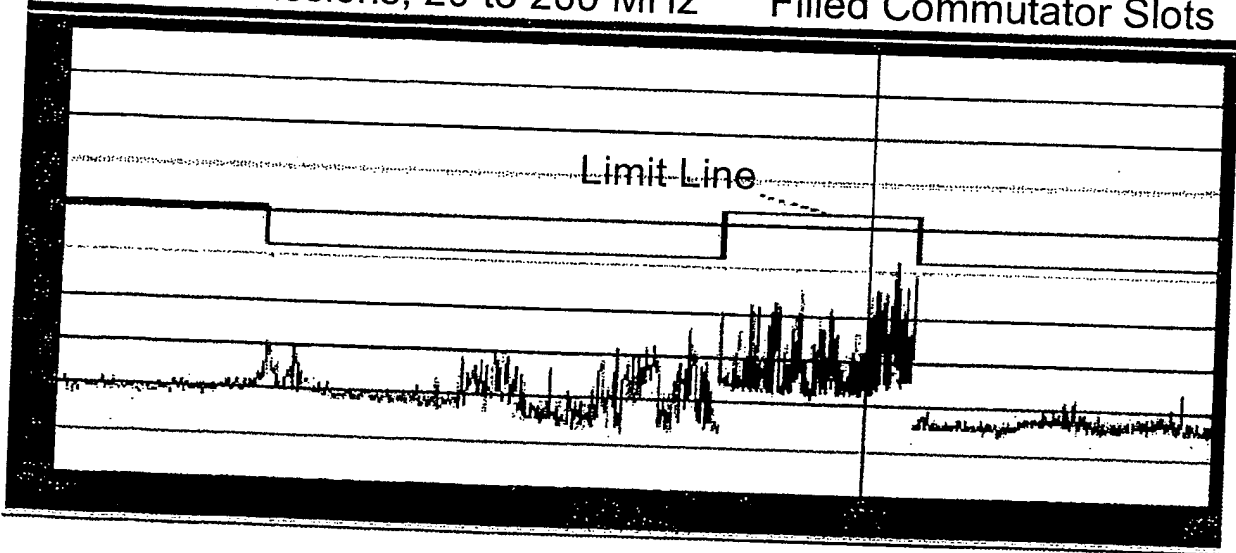


FIG. 8D

Radiated emissions, 20 to 200 MHz Filled Commutator Slots



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FIG. 8E

Radiated emissions, 200 to 1000 MHz Unfilled Commutator Slots

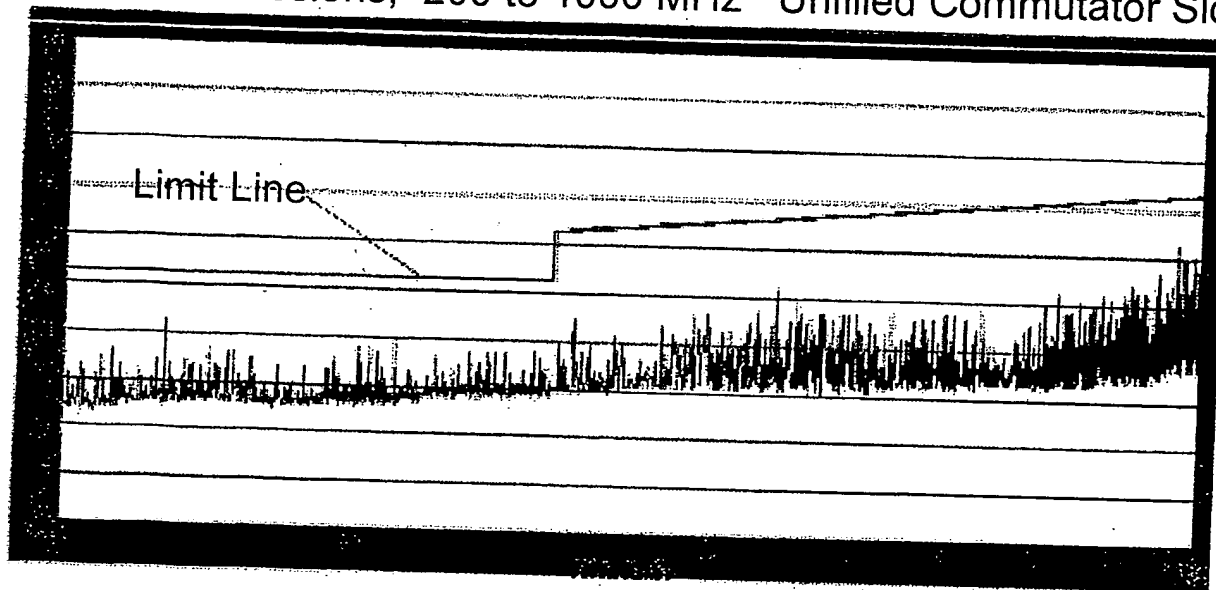
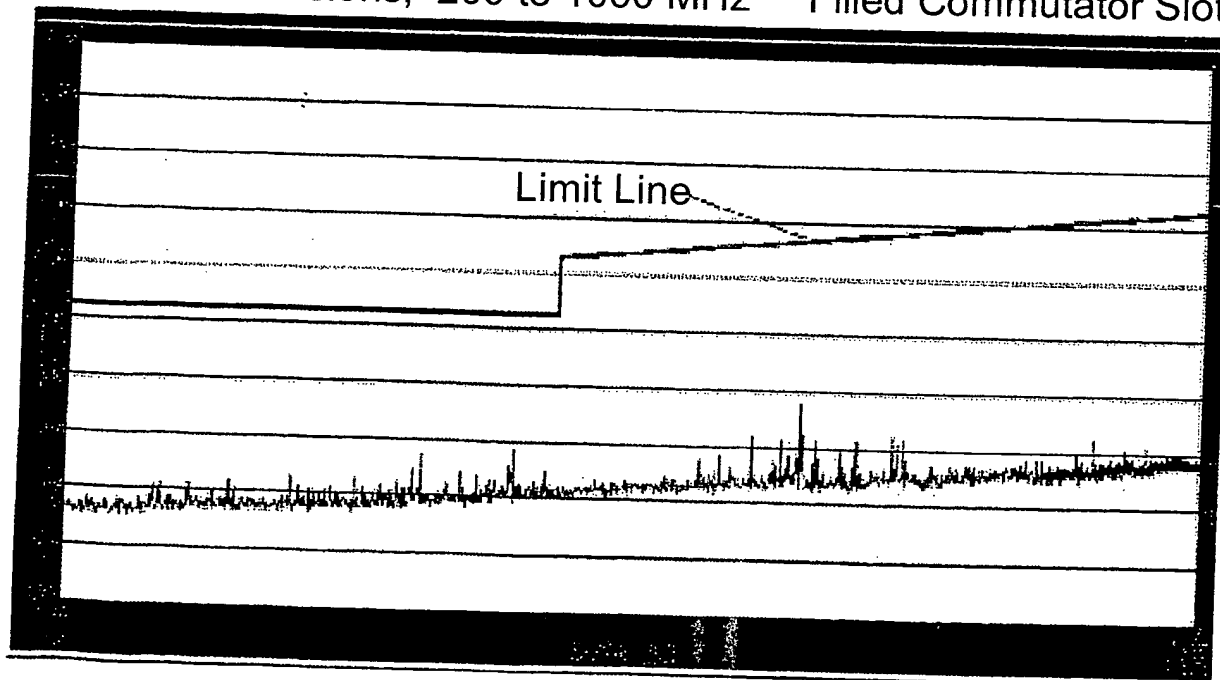


FIG. 8F

Radiated emissions, 200 to 1000 MHz Filled Commutator Slots



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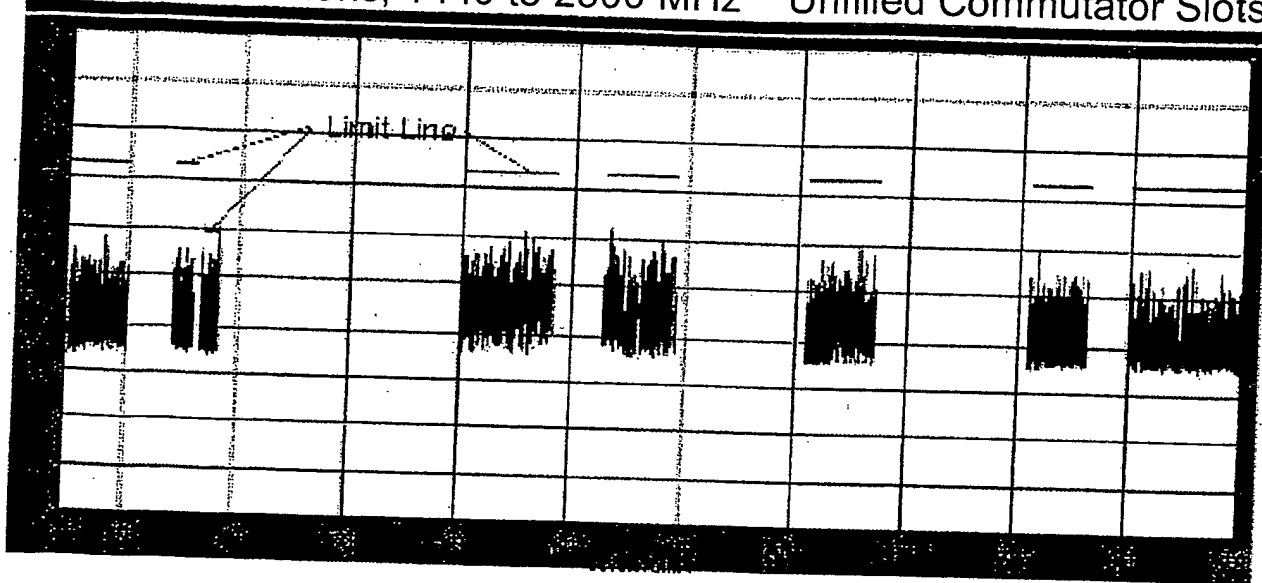
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FIG. 8G

Radiated emissions, 1440 to 2500 MHz Unfilled Commutator Slots



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FIG. 8H

Radiated emissions, 1440 to 2500 MHz Filled Commutator Slots

